Digital electronics (extended candidates)

Mark Scheme 3

Level	IGCSE
Subject	Physics
ExamBoard	CIE
Торіс	Electricity and Magnetism
Sub-Topic	Digital electronics (extended candidates)
Paper Type	(Extended) Theory Paper
Booklet	Mark Scheme 3

Time Allowed:	48 minutes
Score:	/40
Percentage:	/100

Percentage:

1	(a)	(i) N	NOT		B1	
		(ii)	AND		B1	
	(b))	low / 0 / c low / 0 / c		B1 B1	
		(ii)	high / 1 / high / 1 /		B1 B1	
	(c)) Bc	annot prov	ide enough power/voltage/current to light lamp (IGNORE strength)	B1	
	(d) security lamp OR intruder alarm OR burglar alarm with explanation OR beach lighting OR air freezer at indoor ski slope OR fridge alarm i.e. something that switches on when hot and dark (in a practical situation)					[8]
2	(a	ana	llogue	any reading possible/ <u>idea of continuous</u> variation of value of quantity		B1
		digi	ital	idea of two states only		B1
	(b)	if both inputs are 1/high, the output is 1/high only added to previous line				B1
OR if either or both inputs are 0/low, then output is 0/low (accept both answers in form of a truth table)						B1
					[Tota	l: 4]

3	(a)	A B	NOT or inverter AND	B1 B1	
	(b) (accept 1 or ON for HIGH, and 0 or OFF or NOT HIGH for LOW throughout)				
		(i)	A – HIGH and B – LOW (both) no e.c.f.	B1	
		(ii)	A – HIGH and B – HIGH (both) no e.c.f.	B1	
		(iii)	A – LOW and B – LOW (both) no e.c.f.	B1	
	(c)		B cannot provide enough power / current for lamp, or equiv. OR allows remote lamp	B1	
		(ii)	the second one / dark and warm / HIGH, HIGH e.c.f. from (b)	B1	
		(iii)	warning if temperature in a closed / dark space (e.g. refrigerator, kiln) reaches too high a value N.B. "to switch on a lamp when it is dark and warm" not accepted	B1	[8]
4	(a)	NOT	or inverter	B1	
4	(a) (b)		or inverter thermistor NOT thermal resistor	B1 B1	
4	. ,				
4	. ,	(ii)	thermistor NOT thermal resistor	B1	
4	(b)	(ii)	thermistor NOT thermal resistor resistance increases OR voltage across it increases	B1 B1	
4	(b) (c)	(ii)	thermistor NOT thermal resistor resistance increases OR voltage across it increases LOW or 0 or off or NOT HIGH	B1 B1 B1	
4	(b) (c)	(ii) (ii) (iii)	thermistor NOT thermal resistor resistance increases OR voltage across it increases LOW or 0 or off or NOT HIGH (much) larger/ large / higher / high	B1 B1 B1 B1	
4	(b) (c) (d)	(ii) (ii) (iii) to a <u>aut</u>	thermistor NOT thermal resistor resistance increases OR voltage across it increases LOW or 0 or off or NOT HIGH (much) larger/ large / higher / high low temperature e.c.f. from (c) (ii)	B1 B1 B1 B1 B1	

5 (a)	corre sha	ect symbol, must show 3 connections, condone rounded "nose", ignore wid ape, allow OR gate followed by NOT gate, correctly drawn	dth_of_t B1	he:
(b)	eith	ruth table is shown, mark the truth table and ignore the rest ner input 1, output 0 <u>AND</u> both inputs 1, output 0 h inputs 0, output 1 accept high/low, on/off for both	B1 B1	
(c)	(i)	one input is high/1 AND output is low/0 IGNORE any reference to 2nd input	B1	
	(ii)	1. on 2. o	B1 B1	[6]

6	(a)	(i)	LDR correctly identified	B1	
		(ii)	lamp correctly identified	B1	
		(iii)	transistor correctly identified	B1	
	(b)	resis LDR	ore anything that is in terms of currents) tance of LDR becomes high gets larger share of the voltage OR voltage across LDR gets bigger sistor switches/turns lamp on	M1 A1 A1	[6]